

# ADRIÁN FENOLLAR NAVARRO

Computer Engineer and Computer Architect

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## WORK EXPERIENCE

**HUAWEI** | JUNIOR SYSTEM RESEARCHER

July 2022 - September 2022 | Warsaw, Poland

- Research on new techniques for clock synchronization protocols with Huawei Theory Lab (Hong Kong) researchers
- Refactoring, optimization, prototyping and testing of new solutions (C/C++, Python, Bash)

**AED VANTAGE** | INTERN SOFTWARE ENGINEER

March 2021 - August 2021 | Fuente Álamo, Murcia, Spain

- Support to System Validation Team on automatic validation of embedded systems tasks with BMW engineers (Python, Groovy, Bash; CI/CD, QA testing)
- Member of the Tooling team for the automation of internal processes (C#, ASP.NET; CI/CD, DevOps, REST APIs)

## EDUCATION

**UNIVERSITY OF MURCIA**

MSC IN NEW TECHNOLOGIES IN COMPUTER SCIENCE

SPEC. HIGH PERFORMANCE ARCHITECTURES AND SUPERCOMPUTING

2022 - today | Computer Science Faculty, Espinardo Campus, Murcia

- Average Score: 9,83/10 • Honorable mentions: 2

**UNIVERSITY OF MURCIA**

BSC IN COMPUTER ENGINEERING

SPEC. HARDWARE ARCHITECTURE AND SYSTEMS ADMINISTRATION

2018 - 2022 | Computer Science Faculty, Espinardo Campus, Murcia

- Average Score: 9,01/10.0 • Honorable Mentions: 13
- Internal Student in 2020, 2021 and 2022 at Department of Computer Engineering and Technology (DITEC)

## PROJECTS

**MASTER'S DEGREE FINAL PROJECT** | UNIVERSITY OF MURCIA

FLEXMAPPER: A TOOL FOR CHARACTERIZING OPTIMAL MAPPINGS IN

FLEXAGON ARCHITECTURE

September 2022 - today | MSc in Computer Science

- Tutored by [Manuel E. Acacio](#) and [José L. Abellán](#)

**BACHELOR'S DEGREE FINAL PROJECT** | UNIVERSITY OF MURCIA

AUTOMATIC GENERATION OF OPTIMAL MAPPINGS IN FLEXIBLE

ARCHITECTURES FOR DNNs IN STONNE [\[LINK\]](#)

September 2021 - June 2022 | BSc in Computer Engineering

- Score: 10.0/10.0, Honorable Mention
- Tutored by [Manuel E. Acacio](#) and [Francisco Muñoz](#)

**INITIATION TO RESEARCH PROJECT** | UNIVERSITY OF MURCIA

IMPLEMENTATION OF EFFICIENT INFERENCE SYSTEMS IN DNNs

USING GOOGLE EDGE TPU

October 2021 - January 2022 | BSc in Computer Engineering

## TECHNICAL SKILLS

**PROGRAMMING**

C/C++ • Python • Java • Batch/Bash • OpenMP/MPI • CUDA • C# • Groovy • Assembly • System Verilog • JavaScript • HTML/CSS •

**LANGUAGES**

Native: Spanish  
Upper-intermediate: English

**TOOLS**

Windows/Linux • Git/SVN • Maven • CMake • Docker • Kubernetes • OpenStack • SQL • MongoDB • PyTorch

**OTHERS**

Scrum & Agile • DevOps • Microservices • Optimization • SW/HW Validation • Microcontrollers & SoC • Deep Learning

**GENERAL SKILLS**

Positive attitude  
Initiative  
Decision making  
Teamwork  
Leadership  
Fast learning  
Autodidact  
Problem solving

## ACHIEVEMENTS

2023 69th place Southwestern Europe Regional Contest (SWERC) 2022, Milan

2022 2nd place XX Murcia Programming Olympiad

2022 4th place VIII AdaByron National Contest, Madrid

2022 57th place Southwestern Europe Regional Contest (SWERC) 2021, Milan

2022 2nd place VIII AdaByron Regional Contest

2021 2nd place XIX Murcia Programming Olympiad

2021 6th place VII AdaByron National Contest, Online

2020 6th place XVIII Murcia Programming Olympiad

2020 3rd place VI AdaByron Regional Contest, Murcia

## ABOUT ME

MSc Computer Engineering student with a bit of professional experience passionate about computing and new technologies looking for new opportunities and starting a Ph.D. soon. My main interests are computer architecture and artificial intelligence (and if they can be combined, even better!), as well as software development.

Currently, I am collaborating as a researcher with [CAPS](#) group at the University of Murcia on hardware accelerators for deep learning. More specifically, I am working on the development of the [STONNE](#) simulator designing new mappers for the flexible architectures it supports (for DMM and SpMSPm operations). I would like to apply for a summer internship related to my research field, which might also be extended with a collaboration with my university research group. My availability is negotiable depending on the start date of the Ph.D.